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What is claimed is:

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1. A method comprising:
 - trapping initializing data of a first interrupt type to a first interrupt controller;
 - re-routing the initializing data of the first interrupt type to a second interrupt controller; and
 - configuring the second interrupt controller to manage interrupts of the first interrupt type.
 2. The method of claim 1, wherein trapping initializing data of a first interrupt type comprises:
 - configuring a system management interrupt to recognize initializing data of a first interrupt type.
 3. The method of claim 1, wherein initializing data of the first interrupt type comprises a plurality of command words and a first command word begins the initializing of the first interrupt controller, configuring a system management interrupt to recognize initializing data of a first interrupt type and re-route initializing data to the second interrupt controller from the first command word.

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1 4. The method of claim 1, wherein the first interrupt
2 controller comprises an 82C59 controller and the second
3 interrupt controller comprises a advanced programmable interrupt
4 controller.

1 5. A machine readable storage media containing executable
2 program instructions which when executed cause a digital
3 processing system to perform a method comprising:

4 trapping initializing data of a first interrupt type to a
5 first interrupt controller;

6 re-routing initializing data of a first interrupt type to a
7 second interrupt controller; and

8 configuring the second interrupt controller to manage
9 interrupts of the first interrupt type.

1 6. The media of claim 5, wherein trapping initializing data of
2 a first interrupt type comprises:

3 configuring a system management interrupt to recognize
4 initializing data of a first interrupt type.

1 7. The media of claim 5, wherein initializing data of the
2 first interrupt type comprises a plurality of command words and
3 a first command word begins the initializing of the first

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4 interrupt controller, configuring a system management interrupt
5 to recognize initializing data of a first interrupt type and re-
6 route initializing data to the second interrupt controller from
7 the first command word.

1 8. The media of claim 5, wherein the first interrupt
2 controller comprises an 82C59 controller and the second
3 interrupt controller comprises a advanced programmable interrupt
4 controller.

1 9. A system comprising:
2 a central processing unit (CPU);
3 a first bus coupled to the CPU;
4 a first interrupt controller, coupled to the first bus,
5 operable to manage communication with the CPU of interrupts of a
6 first interrupt type;
7 a second bus coupled to the CPU;
8 a second interrupt controller, coupled to the second bus
9 and to the first interrupt controller, operable to manage
10 communication with the CPU of interrupts of a second interrupt
11 type; and
12 a memory coupled to the second interrupt controller
13 comprising a computer-readable medium having a computer-readable

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14 program embodied therein for directing operation of the system,
15 the computer-readable program comprising:
16 instructions for managing interrupts of the first interrupt
17 type by the second interrupt controller.

1 10. The system of claim 9, wherein the computer-readable
2 program further comprises:

3 instructions for trapping initializing data of a first
4 interrupt type to the first interrupt controller;

5 instructions for re-routing initializing data of a first
6 interrupt type to the second interrupt controller; and

7 instructions for configuring the second interrupt
8 controller to manage interrupts of the first interrupt type.

1 11. The system of claim 10, wherein the instructions for
2 trapping initializing data comprise:

3 instructions for configuring a system management interrupt
4 to recognize initializing data of a first interrupt type.

1 12. The system of claim 10, wherein initializing data of the
2 first interrupt type comprise a plurality of command words and a
3 first command word begins the initializing of the first
4 interrupt controller, and the computer-readable program
5 comprises instructions for configuring a system management

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6 interrupt to recognize initializing data of a first interrupt
7 type and re-route initializing data to the second interrupt
8 controller from the first command word.

1 13. The system of claim 9, wherein the first interrupt
2 controller comprises an 82C59 controller and the second
3 interrupt controller comprises a advanced programmable interrupt
4 controller.

1 14. The system of claim 13, wherein the second interrupt
2 controller manages interrupts of the first interrupt type
3 exclusive of the first interrupt controller.

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1 15. A system comprising:

2 a central processing unit (CPU);

3 first means of interrupt processing for managing
4 communication with the CPU of interrupts of a first interrupt
5 type;

6 second means of interrupt processing for managing
7 communication with the CPU of interrupts of a second interrupt
8 type;

9 means for routing interrupts of the first interrupt type to
10 the second interrupt processing means

